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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-30559

Laurence R. Simar, Jr., et al.

Art Unit: 2183

Serial No.: 09/702,320

Examiner: David J. Huisman

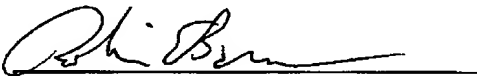
Filed: October 31, 2000

Conf. No.: 9784

For: Very Long Instruction Word Microprocessor With Execution Packet Spanning Two or  
More Fetch Packets With Pre-Dispatch Instruction Selection From Two Latches According to Instruction  
Bit (amended title)

## CERTIFICATION OF FACSIMILE TRANSMISSION

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Robin E. Barnum

September 30, 2005  
Date

## FACSIMILE COVER SHEET

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<input type="checkbox"/> DIVISIONAL APPN	
NAME OF INVENTOR(S): Laurence R. Simar, Jr., et al.	
RECEIPT DATE & SERIAL NO.: Serial No.: <b>09/702,320</b> Filing Date: October 31, 2000	
TITLE OF INVENTION: Very Long Instruction Word Microprocessor With Execution Packet Spanning Two or More Fetch Packets With Pre- Dispatch Instruction Selection From Two Latches According to Instruction Bit (amended title)	
TI FILE NO.: <b>TI-30559</b>	DEPOSIT ACCT. NO.: <b>20-0668</b>
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Texas Instruments Incorporated  
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SEP 30 2005

**RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2183****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Simar et al

Art Unit: 2183

Serial No.: 09/702,320

Examiner: David J. Huisman

Filed: October 31, 2000

Docket: TI-30556

For: VERY LONG INSTRUCTION WORD MICROPROCESSOR WITH EXECUTION  
PACKET SPANNING TWO OR MORE FETCH PACKETS WITH PRE-DISPATCH  
INSTRUCTION SELECTION FROM TWO LATCHES ACCORDING TO  
INSTRUCTION BIT (amended title)

**AMENDMENT UNDER 37 C.F.R. 1.116**


Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**CERTIFICATION OF FAX TRANSMITTAL  
UNDER 37 C.F.R. §1.6(b)**

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Office on September 30, 2005.

  
Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated August 3, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows:

In the Title of the Invention

Please change the Title of the Invention to read --VERY LONG INSTRUCTION WORD MICROPROCESSOR WITH EXECUTION PACKET SPANNING TWO OR MORE FETCH PACKETS WITH PRE-DISPATCH INSTRUCTION SELECTION FROM TWO LATCHES ACCORDING TO INSTRUCTION BIT--.